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Search Results -

Term	Documents
(5 AND 6).USPT.	3
(L6 AND L5).USPT.	3

Database: US Patents Full-Text Database; US Pre-Grant Publication Full-Text Database; JPO Abstracts Database; EPO Abstracts Database; Derwent World Patents Index; IBM Technical Disclosure Bulletins

Search: L7

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Search History

DATE: Thursday, February 06, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=ADJ			
L7	l6 and l5	3	L7
L6	graphic controller or AGP	3360	L6
L5	L4	23	L5
DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L4	L3 same l2	28	L4
L3	(load or loading or store or storing) same (address or addresses) same l1	1191	L3
L2	(controller or (control unit)) adj4 l1	122	L2
L1	translation lookaside buffer or TLB	3625	L1

END OF SEARCH HISTORY

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Search Results -

Term	Documents
SHARE.DWPI,TDBD,EPAB,JPAB,USPT.	104600
SHARES.DWPI,TDBD,EPAB,JPAB,USPT.	25345
SHARING.DWPI,TDBD,EPAB,JPAB,USPT.	63678
SHARINGS.DWPI,TDBD,EPAB,JPAB,USPT.	23
(31 SAME (SHARING OR SHARE)).USPT,JPAB,EPAB,DWPI,TDBD.	0
((SHARE OR SHARING) SAME L31).USPT,JPAB,EPAB,DWPI,TDBD.	0

Database:
- US Patents Full-Text Database
- US Pre-Grant Publication Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search: Refine Search

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Search History

DATE: Thursday, January 30, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L32	(share or sharing) same l31	0	L32
L31	L30	298	L31
DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L30	L29 or GART	328	L30
L29	graphic adj address adj (redirection or remapping or remap or redirecting or redirect) adj table	72	L29
L28	GART same TLB	12	L28
L27	bridge same GART same TLB	2	L27
L26	address adj2 (translation or translating or translator)	7704	L26
L25	bridge same GART	45	L25
L24	bridge same (bus adj controller) same GARt	0	L24
L23	'350' and l22	1	L23
L22	6370624.pn.	2	L22
L21	graphic adj L4	8	L21
L20	l19 and l2 and l1 and (l4 or l5)	1	L20
L19	graphics adj address adj (remmapping or remapping or remap) adj table	72	L19
L18	(memory controller or memory management unit)	29413	L18
L17	L16 and l15	0	L17
L16	(l4 or l5) same l1	107	L16
L15	(l4 or l5) same l2	64	L15
L14	(l4 or l5) same l2 same l1	0	L14
L13	l1 and l2 and (l4 or l5)	65	L13
L12	l11 and l1 and l2 and l3	2	L12
L11	(l4 or l5).ti,ab.	3179	L11
L10	l1 and l2 and l3 and (l4 or l5)	53	L10
L9	l1L8	0	L9
L8	L7 and (l4 or l5)	1	L8
L7	l1 same l2 same l3	42	L7
L6	l1 same l2 same l3 same (l4 or l5)	0	L6
L5	address adj2 (translator or translation or translating)	7704	L5
L4	translation lookaside buffer or TLB	3615	L4
L3	memory controller	26982	L3
L2	graphics adj (processor or controller)	7671	L2
L1	bus controller	8754	L1

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Search Results -

Term	Documents
(14 SAME 13 SAME 1).USPT.	14
(L13 SAME L14 SAME L1).USPT.	14

Database:
- US Patents Full-Text Database
- US Pre-Grant Publication Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search: L15

Refine Search

Recall Text | Clear

Search History

DATE: Wednesday, January 29, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=ADJ			
L15	l13 same l14 same l1	14	L15
L14	(physical or real) adj2 address adj2 register	469	L14
L13	(virtual or linear) adj2 address adj2 register	399	L13
L12	L11 same l10 same l1	15	L12
L11	register adj4 ((linear or virtual) adj2 address)	665	L11
L10	register adj4 ((physical or real) adj2 address)	780	L10
L9	L7 same l6 same l1	300	L9
L8	L7 and l6 and l1	538	L8
L7	register same ((physical or real) adj2 address)	2860	L7
L6	register same ((linear or virtual) adj2 address)	2151	L6
L5	L4 and l2	73	L5
L4	l1.ti,ab.	258	L4
L3	L2	217	L3
DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L2	(compare or comparing or comparison) same register same l1	303	L2
L1	translation lookaside buffer or TLB	3605	L1

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Search Results -

Term	Documents
(9 AND 20).USPT,JPAB,EPAB,DWPI,TDBD.	94
(L9 AND L20).USPT,JPAB,EPAB,DWPI,TDBD.	94

Database: US Patents Full-Text Database; US Pre-Grant Publication Full-Text Database; JPO Abstracts Database; EPO Abstracts Database; Derwent World Patents Index; IBM Technical Disclosure Bulletins

Search: L22

Refine Search

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Search History

DATE: Tuesday, January 28, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L22	l9 and l20	94	L22
L21	L20 and l11	0	L21
L20	translation lookaside buffer or TLB	3300	L20
L19	4638426.pn.	2	L19
L18	(direct memory access or DMA or peripheral or (I adj O) or (input adj output)or graphic) same l9	160	L18
L17	(direct memory access or DMA or peripheral or (I adj O) or (input adj output)or graphics) same l9	160	L17
L16	(direct memory access or DMA or peripheral or (I adj O) or (input adj output)) same l9	142	L16
L15	(direct memory access or DMA or peripheral or (I adj O) or (input adj output) or controller) same l9	272	L15
L14	direct memory access or DMA or peripheral or (I adj O) or (input adj output or controller) same l9	897068	L14
L13	direct memory access or DMA or peripheral or (I adj O) or (input adj output) and l9	897309	L13
L12	graphics and l11	2	L12
L11	l9.ti.	80	L11
L10	L9 and l8	6	L10
L9	(extend or extending or expand or expanding) adj4 (address or addressing)	2579	L9
L8	(table or map) same (translate or translating or convert or converting or translating or mapping or conversion or translation) same ('32' adj bit) same (('36' or '40' or '64') adj bit) same address	83	L8
L7	L6	473	L7
DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L6	(translate or translating or translation or map or mapping) same address same ((extend or extended or expand or expanded or expanding or extending) adj4 (address or address range))	509	L6
L5	(translate or translating or translation or map or mapping) same address same (extend or extended or expand or expanded or expanding or extending) same (address or address range)	2479	L5
L4	l1 and l2	0	L4
L3	L2 same l1	0	L3
L2	graphics address redirection table or GART	320	L2
L1	(extend or extending or expand or expanding) adj4 (address or addressing) adj4 range	176	L1

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Search Results -

Term	Documents
PHYSICAL.USPT.	486072
PHYSICALS.USPT.	326
PAGE.USPT.	206078
PAGES.USPT.	172016
ADDRESS.USPT.	216001
ADDRESSES.USPT.	127549
LARGER.USPT.	900475
LARGERS.USPT.	13
BIGGER.USPT.	14111
BIGGERS.USPT.	178
LONGER.USPT.	666382
((PHYSICAL ADJ2 (PAGE OR ADDRESS)) ADJ4 (LARGER OR BIGGER OR LONGER) SAME ((VIRTUAL OR LOGICAL OR LINEAR) ADJ2 (PAGE OR ADDRESS))).USPT.	25

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Database: US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L25

Refine Search

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Search History

DATE: Tuesday, January 28, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set

DB=USPT; PLUR=YES; OP=ADJ

L25	(physical adj2 (page or address)) adj4 (larger or bigger or longer) same ((virtual or logical or linear) adj2 (page or address))	25	L25
L24	l1 same l19	3	L24
L23	(remap or remapping or mapping or map or translate or convert or translating) same (peripheral or (I adj O) or (input adj output) or DMA or graphic) same address same ((high or upper or ('above' adj2 4GB)) adj4 (memory or address or address space or address range))	138	L23
L22	L21.ti,ab.	32	L22
L21	(remap or remapping or mapping or map or translate or convert or translating) same (peripheral or (I adj O) or (input adj output) or DMA or graphic) same address same (high or upper or ('above' adj2 4GB)) same (memory or address or address space or address range)	618	L21
L20	(extend or extending or expand or expanding) adj4 (address or addressing) adj4 (capability)	64	L20
L19	(extend or extending or expand or expanding) same (address or addressing) same (capability)	678	L19
L18	l10 and l1	32	L18
L17	(concatenate or combine or add) same address same ((expanded or expand or extend or extended) adj address)	38	L17
L16	(convert or converting or translate or translating or map or mapping) same address same ((larger or expanded or extended) adj2 address)	211	L16
L15	l13 same l14	160	L15
L14	((physical or real) adj2 address) same (larger or larger or bigger) same ((linear or logical or virtual) adj2 address)	365	L14
L13	(convert or converting or translate or translating or map or mapping) same ((logical or virtual or linear) adj2 address) same ((physical or real) adj2 address)	3388	L13
L12	L11 and l2	43	L12
L11	L10	432	L11

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

L10	((711/220)!.CCLS.)	453	L10
L9	4638426[uref]	63	L9
L8	4638426.pn.	2	L8
L7	(convert or converting or translate or translating or map or mapping) same (first address) same (larger or large) same (second address)	8	L7
L6	(table or map) same (convert or converting or translate or translating or map or mapping) same l2	338	L6
L5	L4 and l1	4	L5
L4	l2.ti.	260	L4
L3	l1 same l2	44	L3
L2	(extend or extended or extending or expand or expanding or expansion) adj4 (address or addressing)	5027	L2

L1	(translation lookaside buffer or TLB)	3605	L1

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Search Results -

Term	Documents
DIRECT.DWPI,TDBD,EPAB,JPAB,USPT.	948847
DIRECTS.DWPI,TDBD,EPAB,JPAB,USPT.	130436
MEMORY.DWPI,TDBD,EPAB,JPAB,USPT.	1159560
MEMORIES.DWPI,TDBD,EPAB,JPAB,USPT.	133644
MEMORYS.DWPI,TDBD,EPAB,JPAB,USPT.	152
ACCESS.DWPI,TDBD,EPAB,JPAB,USPT.	718382
ACCESSES.DWPI,TDBD,EPAB,JPAB,USPT.	65530
DMA.DWPI,TDBD,EPAB,JPAB,USPT.	24572
DMAS.DWPI,TDBD,EPAB,JPAB,USPT.	435
PERIPHERAL.DWPI,TDBD,EPAB,JPAB,USPT.	859267
PERIPHERALS.DWPI,TDBD,EPAB,JPAB,USPT.	19409
((DIRECT MEMORY ACCESS OR DMA OR PERIPHERAL OR (I ADJ O) OR (INPUT ADJ OUTPUT)OR GRAPHIC) SAME L9).USPT,JPAB,EPAB,DWPI,TDBD.	160

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Search: Refine Search

Recall Text | Clear

Search History

DATE: Tuesday, January 28, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L18	(direct memory access or DMA or peripheral or (I adj O) or (input adj output)or graphic) same l9	160	L18
L17	(direct memory access or DMA or peripheral or (I adj O) or (input adj output)or graphics) same l9	160	L17
L16	(direct memory access or DMA or peripheral or (I adj O) or (input adj output)) same l9	142	L16
L15	(direct memory access or DMA or peripheral or (I adj O) or (input adj output) or controller) same l9	272	L15
L14	direct memory access or DMA or peripheral or (I adj O) or (input adj output or controller) same l9	897068	L14
L13	direct memory access or DMA or peripheral or (I adj O) or (input adj output) and l9	897309	L13
L12	graphics and l11	2	L12
L11	l9.ti.	80	L11
L10	L9 and l8	6	L10
L9	(extend or extending or expand or expanding) adj4 (address or addressing)	2579	L9
L8	(table or map) same (translate or translating or convert or converting or translating or mapping or conversion or translation) same ('32' adj bit) same (('36' or '40' or '64') adj bit) same address	83	L8
L7	L6	473	L7
DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L6	(translate or translating or translation or map or mapping) same address same ((extend or extended or expand or expanded or expanding or extending) adj4 (address or address range))	509	L6
L5	(translate or translating or translation or map or mapping) same address same (extend or extended or expand or expanded or expanding or extending) same (address or address range)	2479	L5
L4	l1 and l2	0	L4
L3	L2 same l1	0	L3
L2	graphics address redirection table or GART	320	L2
L1	(extend or extending or expand or expanding) adj4 (address or addressing) adj4 range	176	L1

END OF SEARCH HISTORY